SFP-AG-LP-85-03 1.25 Gb/s 850 nm Multi-Mode SFP Transceiver

## **SFP Series**

- Up to 1.25 Gb/s data links with DDM
- VCSEL laser transmitter and PIN photo-detector
- Hot-pluggable SFP footprint
- Duplex LC/UPC type pluggable optical interface
- Low power dissipation
- Metal enclosure, for lower EMI
- RoHS compliant and lead-free
- Single +3.3V power supply
- Compliant with SFF-8472



ASCENT's SFP-AG-LP-85-03 Small Form Factor Pluggable (SFP) transceivers are compatible with the Small Form Factor Pluggable Multi-Sourcing Agreement (MSA). The transceiver consists of five sections: the LD driver, the limiting amplifier, the digital diagnostic monitor, the VCSEL laser and the PIN photo-detector. The module data link up to 550 m in 50/125 µm multi-mode fiber.

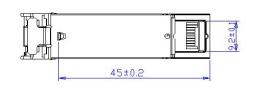
The optical output can be disabled by a TTL logic highlevel input of Tx Disable, and the system also can disable the module via I2C. Tx Fault is provided to indicate that degradation of the laser. Loss of signal (LOS) output is provided to indicate the loss of an input optical signal of receiver or the link status with partner. The system can also get the LOS (or Link)/Disable/Fault information via I2C register access.

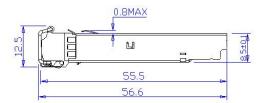


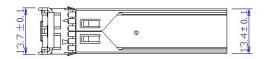
### Key Features -

- Up to 1.25 Gb/s data links with DDM
- VCSEL laser transmitter and PIN photo-detector
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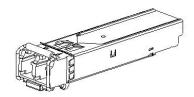
## **Outline Diagram**







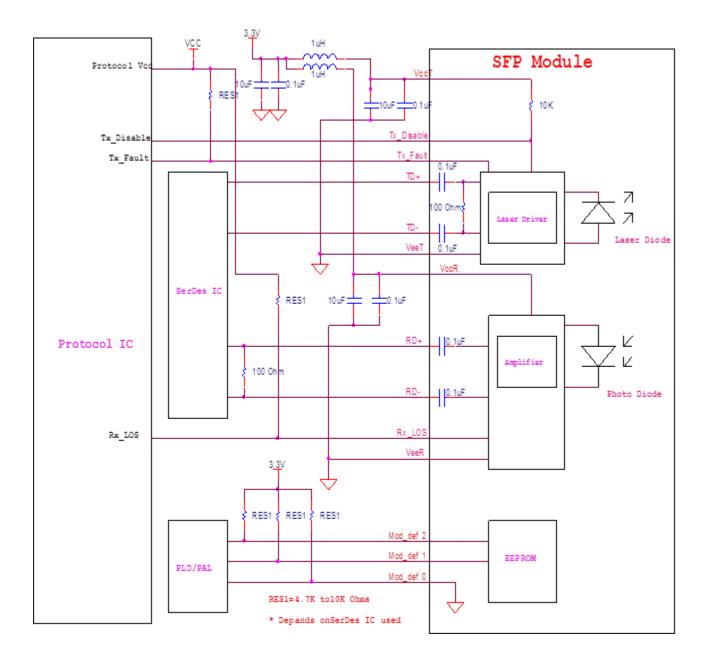
Units in mm



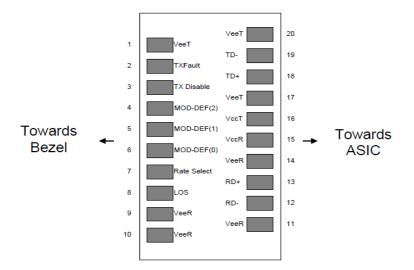
SFP-AG-LP-85-03



## **Recommended Interface Circuit** -



## **Pin Description** -



### Pin out of Connector Block on Host Board

Pin	Symbol	Name/Description	Note
1	VEET	Transmitter Ground (Common with Receiver Ground)	1
2	TFAULT	Transmitter Fault.	
3	Tdis	Transmitter Disable. Laser output disabled on high or open.	2
4	MOD_DEF(2)	Module Definition 2. Data line for Serial ID.	3
5	MOD_DEF(1)	Module Definition 1. Clock line for Serial ID.	3
6	MOD_DEF(0)	Module Definition 0. Grounded within the module.	3
7	Rate Select	No connection required	4
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation.	5
9	Veer	Receiver Ground (Common with Transmitter Ground)	1
10	Veer	Receiver Ground (Common with Transmitter Ground)	1
11	Veer	Receiver Ground (Common with Transmitter Ground)	1
12	RD-	Receiver Inverted DATA out. AC Coupled	
13	RD+	Receiver Non-inverted DATA out. AC Coupled	
14	Veer	Receiver Ground (Common with Transmitter Ground)	1
15	Vccr	Receiver Power Supply	
16	Vcct	Transmitter Power Supply	
17	VEET	Transmitter Ground (Common with Receiver Ground)	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled.	
19	TD-	Transmitter Inverted DATA in. AC Coupled.	
20	Veet	Transmitter Ground (Common with Receiver Ground)	1

#### Notes:

1. Circuit ground is internally isolated from chassis ground.

2. Laser output disabled on  $T_{DIS}$  >2.0V or open, enabled on  $T_{DIS}$  <0.8V.



3. Should be pulled up with 4.7 k $\Omega$  to 10 k $\Omega$  on host board to a voltage between 2.0V and 3.6V.MOD\_DEF (0) pulls line low to indicate module is plugged in.

4. This is an optional input used to control the receiver bandwidth for compatibility with multiple data rates (most likely Fiber Channel 1x and 2x Rates). If implemented, the input will be internally pulled down with >  $30k\Omega$  resistor. The input states are:

- Low (0 0.8V): Reduced Bandwidth
- (>0.8, < 2.0V): Undefined
- High (2.0 3.465V): Full Bandwidth
- Open: Reduced Bandwidth

5. LOS is open collector output should be pulled up with  $4.7k - 10k\Omega$  on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.

## **Specifications** -

#### **Absolute Maximum Ratings**

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Storage Temperature	Ts	-40		85	°C	
Relative Humidity	RH	5		95	%	
Power Supply Voltage	Vcc	-0.5		4	V	
Signal Input Voltage		-0.3		Vcc+0.3	V	
Receiver Damage Threshold		+5			dBm	

#### **Recommended Operating Conditions**

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Case Operating Temperature	$T_{case}$	0		70	°C	SFP-AG-LP-85-03
		-10		80		SFP-AG-LP-85-03-E
		-40		85		SFP-AG-LP-85-03-A
Power Supply Voltage	V <sub>cc</sub>	3.13	3.3	3.47	V	
Power Supply Current	lcc			280	mA	
Power Supply Noise Rejection				100	mVp-p	100 Hz to 1 MHz
Data Rate			1250/1250		Mbps	TX Rate/RX Rate
Transmission Distance				550	Μ	
Coupled Fiber	Multi-mo	de fiber				50/125 µm MMF

#### **Transmitter Specifications**

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Average Output Power	Роит	-9		-3	dBm	1
Extinction Ratio	ER	9			dB	
Center Wavelength	$\lambda_{C}$	830	850	860	nm	VCSEL Laser
Spectrum Bandwidth (RMS)	σ			0.85	nm	
Transmitter OFF Output Power	P <sub>Off</sub>			-45	dBm	

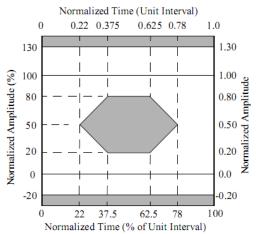


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Differential Line Input Impedance	RIN	90	100	110	Ω
Output Eye Mask	Compliant with IEEE802.3 z (class 1 laser safet				

#### Notes

- 1. Measure at 2^7-1 NRZ PRBS pattern
- 2. Transmitter eye mask definition



#### **Receiver Specifications**

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Input Optical Wavelength	$\lambda_{\text{IN}}$	770	850	860	nm	PIN-TIA
Receiver Sensitivity	P <sub>IN</sub>			-17	dBm	1
Input Saturation Power (Overload)	P <sub>SAT</sub>	0			dBm	
Loss Of Signal Assert	PA			-18	dBm	
Loss Of Signal De-assert	PD	-35			dBm	2
LOS Hysteresis	PA-PD	0.5	2	6	dB	

#### Notes:

- 1. Measured with Light source 850nm, ER=9dB; BER =<10^-12 @PRBS=2^7-1 NRZ
- 2. When LOS de-asserted, the RX data± output is High-level (fixed)

#### **Electrical Interface Characteristics**

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Transmitter						
Total Supply Current	Icc			А	mA	1
Transmitter Disable Input-High	V <sub>DISH</sub>	2		Vcc+0.3	V	
Transmitter Disable Input-Low	V <sub>DISL</sub>	0		0.8	V	
Transmitter Fault Input-High	VTxFH	2		Vcc+0.3	V	
Transmitter Fault Input-Low	VTxFL	0		0.8	V	
Receiver						
Total Supply Current	lcc			В	mA	1
LOSS Output Voltage-High	VLOSH	2		Vcc+0.3	V	LVTTL



V

LOSS Output Voltage-Low VLOSL 0 0.8

#### Notes

1. A (TX) + B (RX) = 280mA (Not include termination circuit)

#### **Regulatory Compliance**

Feature	Reference	Performance
Electrostatic discharge(ESD)	IEC/EN 61000-4-2	Compatible with standards
Electromagnetic Interference (EMI)	FCC Part 15 Class B EN 55022 Class B	Compatible with standards
	(CISPR 22A)	
Laser Eye Safety	FDA 21CFR 1040.10, 1040.11 IEC/EN	Class 1 laser product
	60825-1, 2	
Component Recognition	IEC/EN 60950 , UL	Compatible with standards
ROHS	2002/95/EC	Compatible with standards
EMC	EN61000-3	Compatible with standards

### **Ordering Information**

Product part	Data Rate	Media	Wavelength	Transmission	Temperatu	re
Number	(Mbps)		(nm)	Distance (m)	Range(Tcas	e)(°C)
SFP-AG-LP-85-03	1250	Multi-mode fiber	850	550	0 to 70	commercial
SFP-AG-LP-85-03-E	1250	Multi-mode fiber	850	550	-10 to 80	extended
SFP-AG-LP-85-03-A	1250	Multi-mode fiber	850	550	-40 to 85	industrial

## **Digital Diagnostic Functions**

ASCENT SFP-AG-LP-85-03 transceivers support the 2-wire serial communication protocol as defined in the SFP MSA. It is very closely related to the E2PROM defined in the GBIC standard, with the same electrical specifications.

The standard SFP serial ID provides access to identification information that describes the transceiver's capabilities, standard interfaces, manufacturer, and other information.

Additionally, ASCENT SFP transceivers provide a unique enhanced digital diagnostic monitoring interface, which allows real-time access to device operating parameters such as transceiver temperature, laser bias current, transmitted optical power, received optical power and transceiver supply voltage. It also defines a sophisticated system of alarm and warning flags, which alerts end-users when particular operating parameters are outside of a factory set normal range.

The SFP MSA defines a 256-byte memory map in E2PROM that is accessible over a 2-wire serial interface at the 8 bit address 1010000X (A0h). The digital diagnostic monitoring interface makes use of the 8 bit address



1010001X (A2h), so the originally defined serial ID memory map remains unchanged. The interface is identical to, and is thus fully backward compatible with both the GBIC Specification and the SFP Multi Source Agreement.

The operating and diagnostics information is monitored and reported by a Digital Diagnostics Transceiver Controller (DDTC) inside the transceiver, which is accessed through a 2-wire serial interface. When the serial protocol is activated, the serial clock signal (SCL, Mod Def 1) is generated by the host. The positive edge clocks data into the SFP transceiver into those segments of the E2PROM that are not write-protected. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA, Mod Def 2) is bidirectional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

Digital diagnostics for the SFP-AG-LP-85-03 are internally calibrated by default.

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